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APPLICATION NO		FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.	
10/633,830 08/04/2003		08/04/2003	Jeffrey D. Stroomer	X-1387 US	5273	
24309	7590	12/07/2005		EXAMINER		
XILINX,			DINH, PAUL			
ATTN: LE	GAL DEP	PARTMENT			· · · · · · · · · · · · · · · · · · ·	
2100 LOG	IC DR		ART UNIT	PAPER NUMBER		
SAN JOSE	, CA 95	124	2825	2825		

DATE MAILED: 12/07/2005

Please find below and/or attached an Office communication concerning this application or proceeding.

		Application No) .	Applicant(s)				
		10/633,830		STROOMER ET AL.				
	Office Action Summary	Examiner		Art Unit				
		Paul Dinh		2825				
	The MAILING DATE of this communicat	tion appears on the cov	er sheet with the c	orrespondence add	dress			
Period fo	· ·							
WHIC - Exter after - If NC - Failu Any	ORTENED STATUTORY PERIOD FOR CHEVER IS LONGER, FROM THE MAIL nations of time may be available under the provisions of 31 SIX (6) MONTHS from the mailing date of this communic period for reply is specified above, the maximum statuto re to reply within the set or extended period for reply will, reply received by the Office later than three months after the patent term adjustment. See 37 CFR 1.704(b).	ING DATE OF THIS C 7 CFR 1.136(a). In no event, hor ation. ry period will apply and will expir by statute, cause the application	COMMUNICATION wever, may a reply be tim re SIX (6) MONTHS from to become ABANDONEI	I. lely filed the mailing date of this co D (35 U.S.C. § 133).				
Status								
1)⊠	Responsive to communication(s) filed o	n <u>04 August 2003</u> .						
2a)	This action is FINAL . 2b) This action is non-final.							
3)□	Since this application is in condition for allowance except for formal matters, prosecution as to the merits is							
	closed in accordance with the practice u	under <i>Ex parte Quayle</i>	, 1935 C.D. 11, 45	33 O.G. 213.				
Dispositi	ion of Claims							
4)⊠	Claim(s) <u>1-24</u> is/are pending in the appl	ication.						
•	4a) Of the above claim(s) is/are withdrawn from consideration.							
	Claim(s) <u>12-15</u> is/are allowed.							
6)⊠	6)⊠ Claim(s) <u>1,2,4,6,11,16-19,21 and 23</u> is/are rejected.							
7)🛛	Claim(s) <u>3,5,7-10,20,22 and 24</u> is/are o	bjected to.						
8)[Claim(s) are subject to restriction	and/or election requir	ement.					
Applicati	on Papers							
9)	The specification is objected to by the E	xaminer.						
10)🖂	The drawing(s) filed on <u>04 August 2003</u>	is/are: a)⊠ accepted	or b) ☐ objected t	o by the Examiner	·.			
	Applicant may not request that any objection	n to the drawing(s) be hel	d in abeyance. See	e 37 CFR 1.85(a).				
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).								
11)	The oath or declaration is objected to by	the Examiner. Note th	e attached Office	Action or form PT	O-152.			
Priority u	ınder 35 U.S.C. § 119							
12)	Acknowledgment is made of a claim for	foreign priority under 3	5 U.S.C. § 119(a)	-(d) or (f).				
	☐ All b)☐ Some * c)☐ None of:							
	1. Certified copies of the priority doc	cuments have been rec	eived.					
	2. Certified copies of the priority documents have been received in Application No							
	3. Copies of the certified copies of the priority documents have been received in this National Stage							
	application from the International	·						
* \$	See the attached detailed Office action fo	or a list of the certified o	opies not receive	d.				
Attachmen —	, ,		_					
	e of References Cited (PTO-892)		Interview Summary (Paper No(s)/Mail Da					
3) 🔲 Infor	e of Draftsperson's Patent Drawing Review (PTO- nation Disclosure Statement(s) (PTO-1449 or PTC r No(s)/Mail Date)/SB/08) 5) □		atent Application (PTO	-152)			

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DETAILED ACTION

Claim Rejections - 35 USC § 102

The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form The basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless -

- (b) The invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.
 - Claims 1-2, 4, 6, 11, 16-19, 21 and 23 are rejected under 35 U.S.C. 102(b) as being anticipated by Duboc Et Al (USP 6425116)

(Claim 1 and similarly recited claims 16, 18)

Generating HDL code for one or more of a plurality of high-level subsystems in a High-level design tagged by a user for HDL code generation (col 6 lines 3-16, fig 1-2, 5, 7); and

Reusing previously generated HDL code instead of generating new HDL code for each high-level subsystem tagged by the user for HDL code reuse generation (col 6 lines 3-16, fig 1-2, 5, 7).

(Claims 2, 17, 19) detecting whether one or more attributes of a high-level subsystem have changed since HDL code for the high-level subsystem was previously generated; and if one or more attributes of the high-level subsystem have changed, then generating new HDL code for the high-level subsystem in place of the previously generated HDL code (fig 2-6).

(Claims 4, 21) further comprising, for each high-level subsystem for which HDL code has been generated, simulating a selected one of the high-level subsystem and the HDL code in response to a user-specified selection (col 10 line 58+, col 11, fig 2, 5, 7).

(Claim 6, 23) further comprising providing for each high-level subsystem first and second user configurable parameters (fig 1-2, 5-7), wherein a state of the first parameter selects Between generating HDL code from a high-level subsystem and reusing previously generated HDL code for the high-level subsystem, and a state of the second parameter selects between simulating the high-level subsystem and simulating HDL code associated with the high-level subsystem (col 6-8, 10-11).

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(Claim 11) further comprising presenting to the user a configurable high-level subsystem that includes a library of blocks (fig 2-3, 5), a user-configurable first parameter that selects one of the blocks to be used in generating HDL code (fig 1-2, 5-7), a user-configurable second parameter that controls reuse of previously generated HDL code (col 6 lines 3-16, fig 1-2, 5, 7).

, and a user-configurable third parameter that controls selecting between a high-level subsystem and generated HDL code for simulation (col 10-11, fig 2, 5, 7).

2. Claim 1 and similarly recited claims 16, 18are rejected under 35 U.S.C. 102(b) as being anticipated by Panchul Et Al (US Patent Publication No. 2001/0034876)

Generating HDL code for one or more of a plurality of high-level subsystems in a High-level design tagged by a user for HDL code generation (fig 2, para 0016, 0102, 0109, 0182); and

Reusing previously generated HDL code instead of generating new HDL code for each high-level subsystem tagged by the user for HDL code reuse generation (para 0029, 0179).

Allowable Subject Matter

Claims 3, 5, 7-10, 20, 22, and 24 are objected to as being dependent upon a rejected base claim, but would be allowable if rewritten in independent form including all of the limitations of the base claim and any intervening claims.

Claims 12-15 are allowed.

Claims 3, 5, 7-10, 20, 22, and 24 be allowable and Claims 12-15 are allowed because the prior art of record does not teach or suggest the limitations in:

Claim 3 and similarly recited claim 20.

Claim 5 and similarly recited claim 22.

Claim 7 and similarly recited claims 12, 24.

Conclusion

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Paul Dinh whose telephone number is 571-272-1890. The examiner can normally be reached on Monday to Friday from 8:30am to 5:00pm.

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If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Matthew S. Smith can be reached on 571-272-1907. The fax number for the organization where this application or proceeding is assigned is 571-273-8300.

Paul Diul

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

Paul Dinh

Patent Examiner